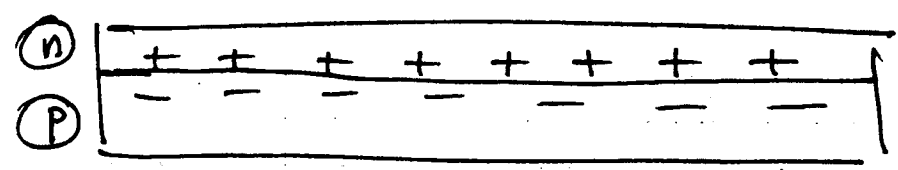


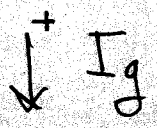
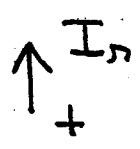
Lecture 12

Solar Cell in the dark,
i.e. n-p junction in the dark



There is no net current in the cell.

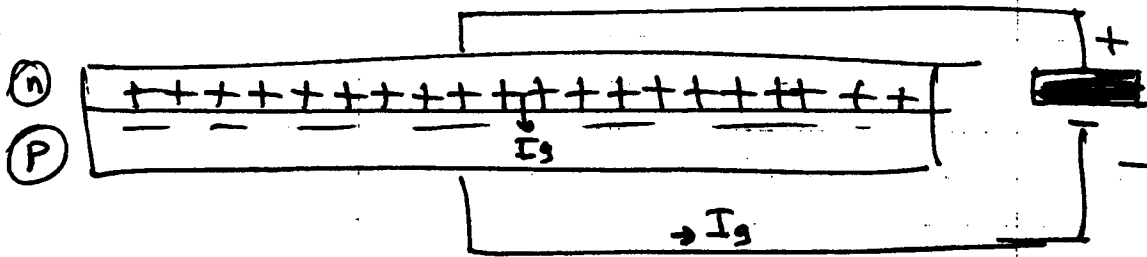
There is a balance of two currents



Recombination Current.
 Net flow of majority carrier, i.e. flow of wandering holes from P to n, seeking an electron, and flow of wandering electrons from n to P, seeking a positive charge.

Generation Current.
 Net flow of minority carriers from generation of electrons and holes. Created from vibrations - from heat.

Suppose we apply an external voltage to make the n-side more positive, the p-side more negative

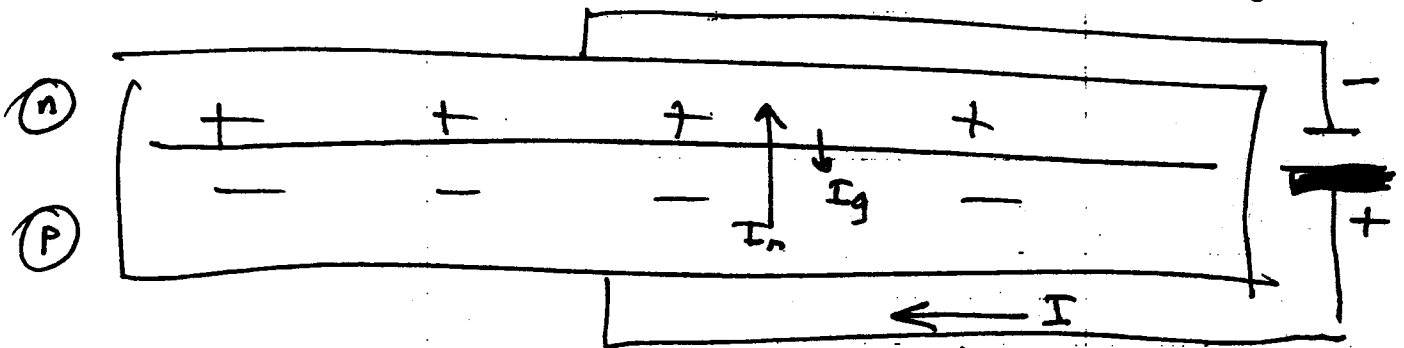


We have made ^{net} upward diffusion of (+) more difficult -- has to fight increased voltage Reverse bias

$I_n \rightarrow 0$

I_g remains -- small downward current

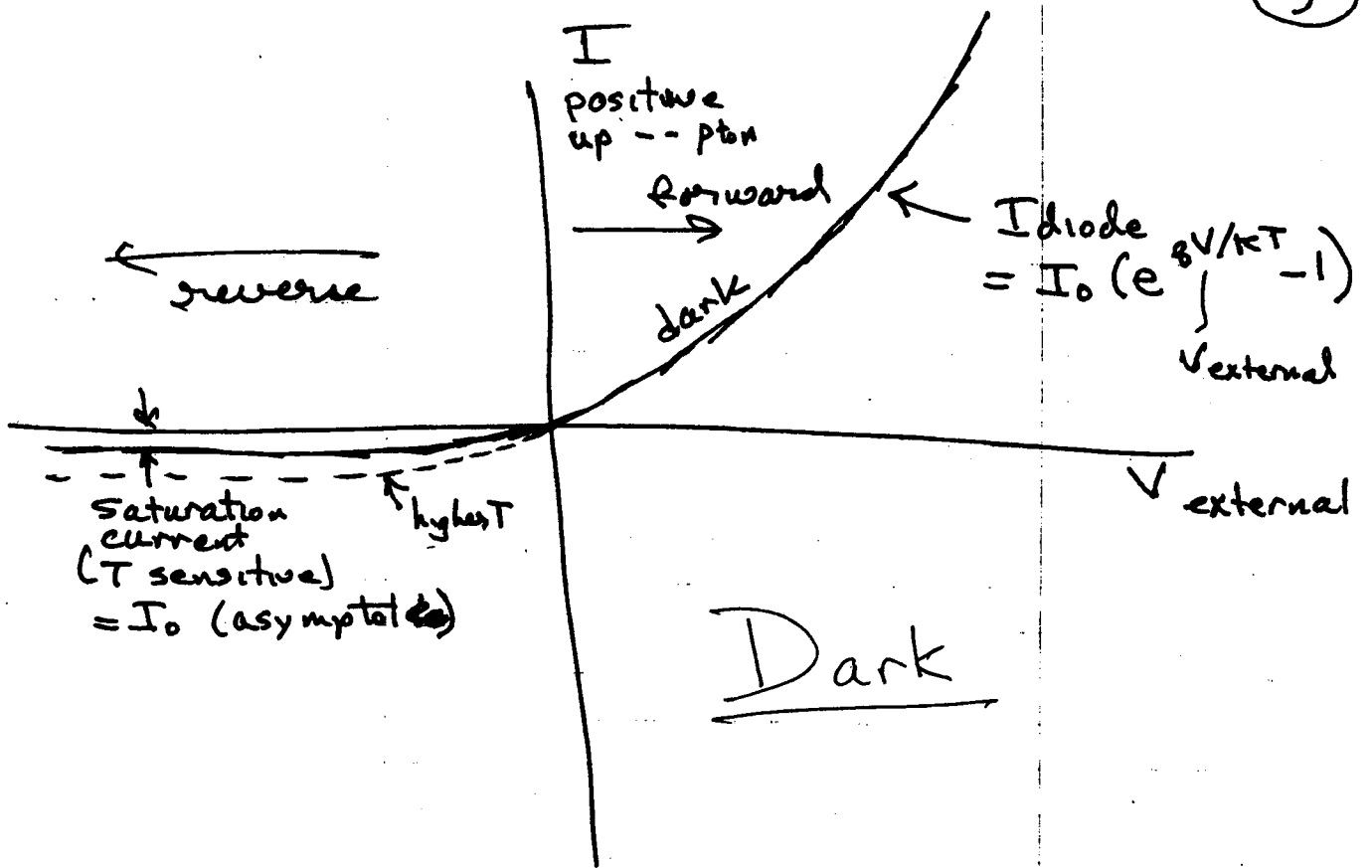
Now, switch the external voltage



We have made net ^{upward} diffusion of (+) easier

$I_n \gg I_g$

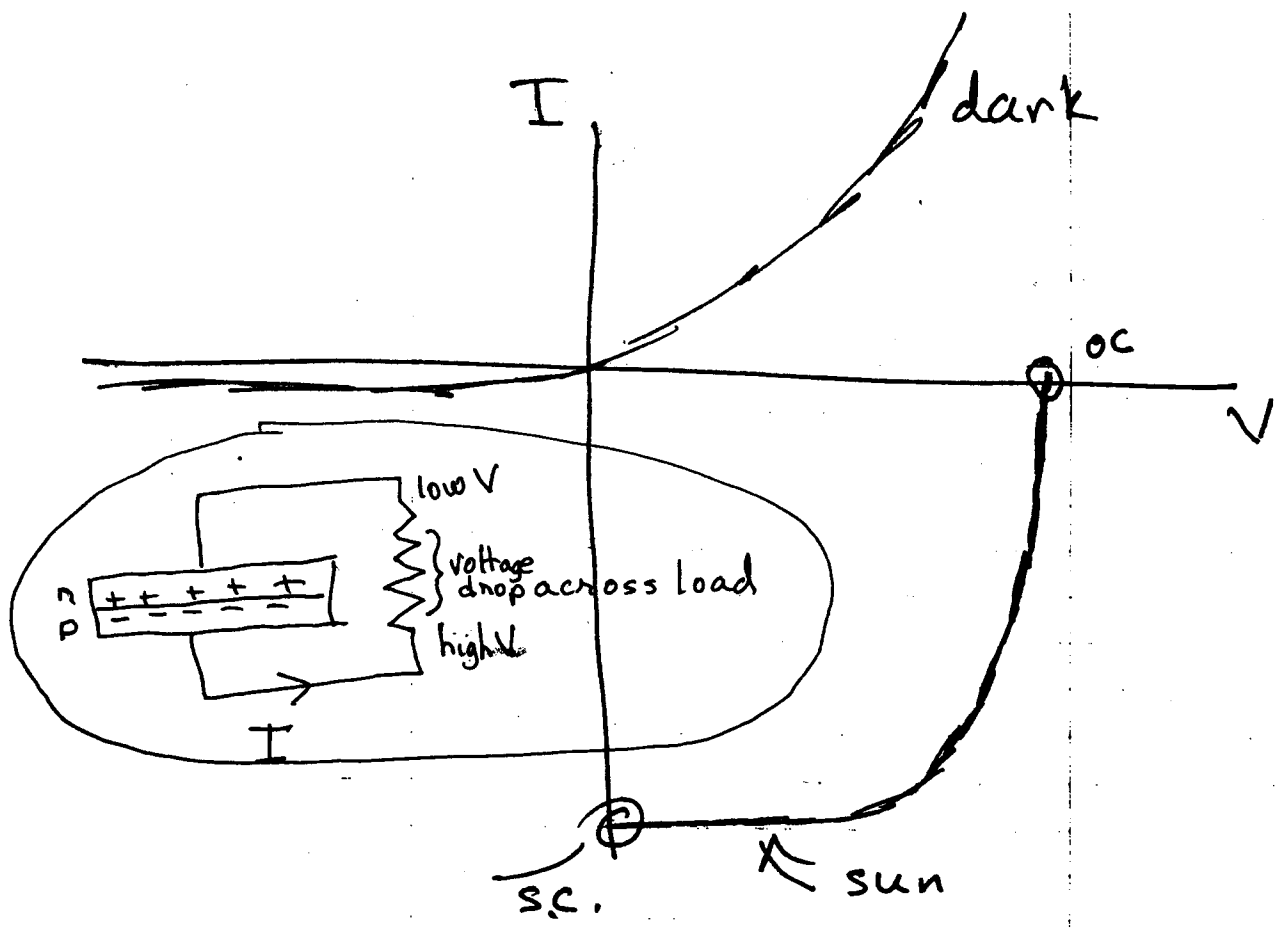
Forward bias



Now let the sun fall on the solar cell (n-p junction). The sun light creates a downward (n to p) current. This is a negative current on our diagram.

$$I = I_{diode} - I_{sun} = I_0 (e^{\frac{qV}{kT}} - 1) - I_{sun}$$

The voltage is like the forward biasing case; since there is a voltage drop across the load, the voltage on P-side > voltage on n-side



If the load is a very thick wire (very small resistance) connecting the p to n side, the Voltage drop is essentially zero — — short circuit.

Then $I_{diode} = I_0 (e^{\frac{qV}{kT}} - 1) = I_0(1-1) = 0$
 and $I = I_{load} = -I_{sun} = I_{sc}$

Max current, but since $V = 0$,
 Power = $IV = 0$. No electrical work is being done on load.

As the resistance of the load is increased, its voltage drop increases, V increases in our

equation and I_{diode} increases. The net I (I through load) is starting to drop (become more upwards in cell, moving towards $I=0$).

However, we don't notice this effect much until the external resistance becomes very large, i.e., ^{as} we approach "open circuit".

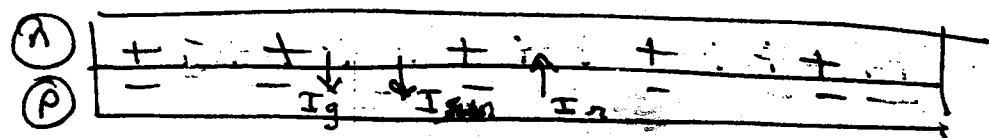
At open circuit, $I_{Load} = 0$

and

$$I = 0 = I_0 (e^{qV/KT} - 1) - I_{sun}$$

$$\text{Thus } I_0 (e^{qV/KT} - 1) = I_{sun} = -I_{sc}$$

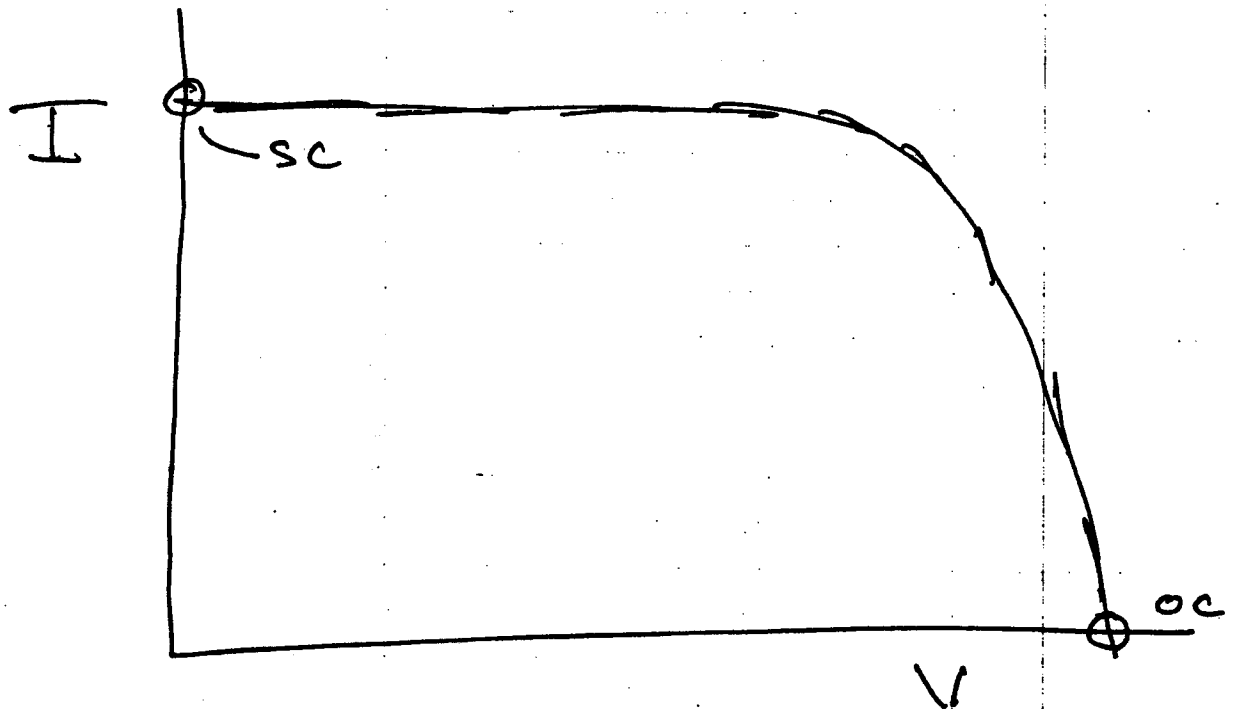
sun \downarrow V_{oc}



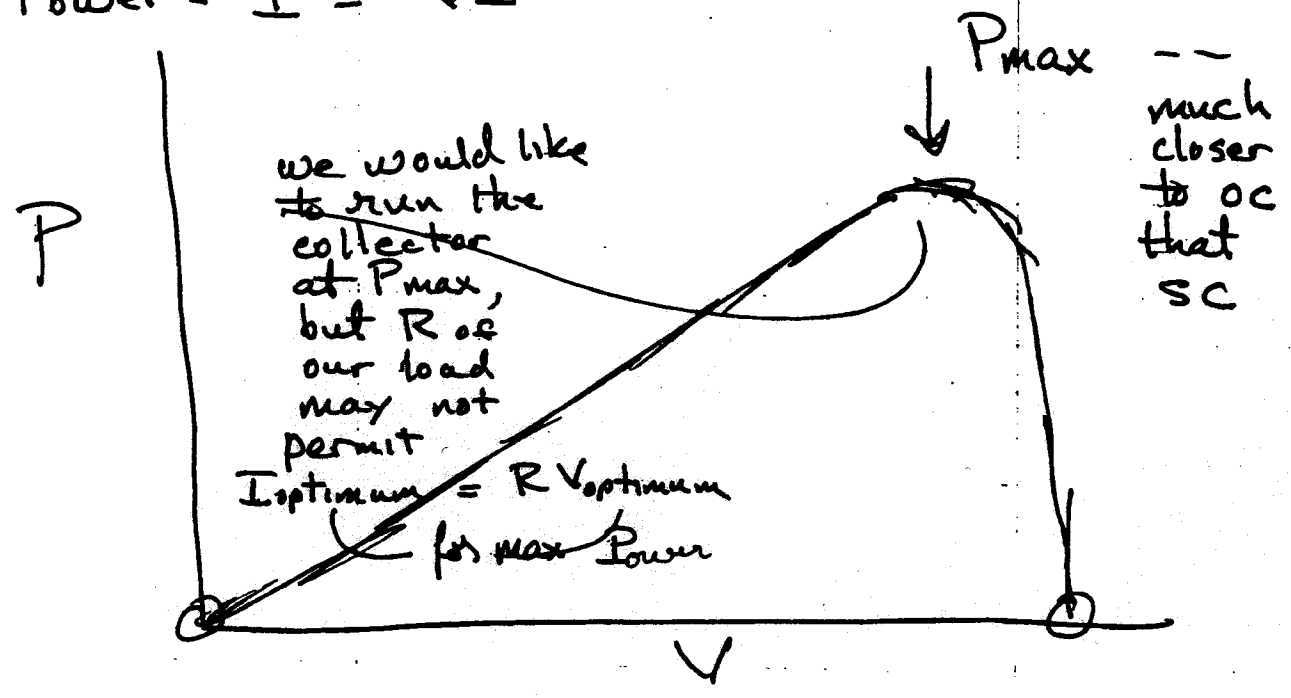
essentially (since I_g is small), we have balance of I_{sun} with I_{sc}

Redo the plot:
Switch I_{load} to positive

$$I = I_{load} = I_{sun} - I_{diode}$$
$$= I_{sun} - I_0(e^{qV/kT} - 1)$$



$$Power = P = VI$$



There are "trackers" that permit running at P_{max} -- they adjust R to keep system at P_{max} -- electroc power tracker

Called ~~output~~ power point tracking

instead of sun tracker

Cell to Panel:

Panel is a number of cells wired in series, typically 30 to 40 cells

$$V_{panel} = V_{cell} \times \text{number of cells}$$

$$I_{panel} = I_{cell}$$

Typical size (for largest of panels)

Panel $\sim 1m^2$

Cell $\sim 250cm^2$

16 cm x 16 cm
or 18 cm diameter

For panel, want to know

- V_{oc}
- I_{sc}
- $V_{maxP} \sim .75 V_{oc}$
- I_{maxP}
- P_{max}

for rated sunlight
and panel $T=25^{\circ}C$

See page 96 of
text for example.

Cell rated @ $25^{\circ}C$

AM 1.5 radiation of 1000 W/m^2

$$1.5 = \frac{1}{\cos \theta_z}$$

$\theta_z = 48^{\circ}$

Note: since I_0 increases with T ,
 I_{diode} increases with T
 and I_{load} drops with T

For crystalline Si cells,

0.4% drop in Power for each
degree C above $25^{\circ}C$.

A cold, strongly radiated cell
would give the most power.

9

Factors affecting efficiency:

1. Top surface contact obstruction: loss $\sim 3\%$
2. Top surface reflection: loss $\sim 1\%$
3. Photon energy insufficient: loss $\sim 23\%$
4. Excess photon energy: loss $\sim 33\%$
5. Voltage factor - voltage produced at junction less than band gap energy: loss $\sim 20\%$

6. Curve factor loss #1
i.e. $P \neq V_{oc} \times I_{sc}$

$$\text{Fill factor} = \frac{P_{max}}{V_{oc} I_{sc}} \quad (.88 \text{ max for silicon})$$

loss $\sim 4\%$

7. Curve factor loss #2

$$I = I_{sun} - I_0 (e^{qV/KT} - 1)$$

not quite followed

loss $\sim 5\%$

Delivered Power Si

10 to 17%

System Cost

Panels	Cost/m ²	Efficiency
Single-crystal Si	↓ decreasing	↓ decreasing
Multi & poly crystal Si		
Thin film Si		
Thin film alloys		

Thus Cost/watt doesn't change that much, stays around \$4.5 → \$5.5/Watt peak for small systems.

Maybe \$3.50/w_p for large systems

Balance of System and its Cost

(11)

How much electrical power?

Example:

Specify a 8 panel,
1000 watt system for your
home.

This means in rated sunlight
of 1000 w/m^2 normal to the
panels, ^{and for panel $T = 25^\circ\text{C}$} the system would
produce 1000 watts (1 kw)
of electricity.

Suppose the efficiency of the
panels based on full area is
12.5%.

$$1000 \text{ w/m}^2 \times A \times 0.125 = 1000 \text{ w}$$

$$\Rightarrow A = 8 \text{ m}^2$$

$$\text{or } A_{\text{one panel}} = 1 \text{ m}^2$$

Sunlight:

$$1000 \text{ w/m}^2 \times 8 \text{ m}^2 = 8000 \text{ w}$$

Electricity:

$$1000 \text{ w}$$

On average, it is daylight only $\frac{1}{2}$ the time, 12 hrs. But the early and late hours of the day don't offer much solar energy, especially for panels set at one tilt angle, so we are down to about 8 hrs of useful daylight

Thus $1000 \text{ w} \times \frac{8}{24} = 333$ average watts

Now, it is cloudy some of the time. For a place like Phoenix, the result is about 200⁺ watts average power (i.e. 20⁺% capacity factor)

For place like Seattle,
capacity factor is about
12% \implies 120 watts
average

The 8 panels probably cost
at least \$4000 -- maybe
as much as \$6000,
and we need balance of system:
biggest item is inverter

Total Cost: \$7000 to \$12000
for our 1000 watts system

Simple cost

$$\frac{120 \text{ watts} \times 8760 \text{ hr/yr} \times 20 \text{ yrs}}{\$7000 - \$12000}$$

$$\implies 12 \text{ ¢/kwh} - 57 \text{ ¢/kwh}$$

Phoenix ^(200, 30) Seattle ^(120, 20, \$12K)
@ best case (\$7K) @ worst case

SYSTEM

