Instruction Timing  
(See Notes)


the wait() function from lab #3
- time delay

the op codes (instruction) used

1dr, str  - 2 clock cycles
add, sub  - 1 clock cycles

cmp, nop  - 1 clock cycles
bne      - 0 clock cycles
push ldwfd - 2 clock cycles

load/store register

compare no operation

branch if not equal

stack operations

I suggest that you copy the assembly language
from the assignment to a Spreadsheet to
compute the total number of cycles \( \Rightarrow \) execution time

The clock frequency is 667 MHz