Homework #1, ME/MSE 485, due on Jan. 18, 2011

1. A chip of 120 W is connected to a series of components vertically, see Fig. 1. The upper components are thermal interface material (TIM), heat sink and convective air (its temperature, $T_{air} = 25^\circ C$). The lower components are solder bumps (its total number is 10 units) surrounded by interface filler polymer (ifp), then to the bottom printed circuit board (PCB) whose temperature is set equal to $T_{PCB} = 30^\circ C$. Assume that the area of each component, i.e chip, TIM, heat sink, is 2 cm x 2 cm, i.e., $A=4 \text{ cm}^2$. Answer to the following questions.

1. Compute the thermal resistance of the convected air flow, $R_{air}$, by using Fig. 2 for a single module with air speed of 2m/sec where you have to estimate $h$ from Fig. 2, first.

   \[ R_{air} = \frac{1}{hA} = \frac{1}{30 \times 4 \times 10^{-4}} = 83.333 \ (K/W) \]

2. Compute the thermal resistance of heat sink, $R_{hs}$, with thickness of aluminum heat sink being 3 mm, using the thermal conductivity value of aluminum ($K_{aluminum}=230 \ W/(m.K)$).

   \[ R_{hs} = \frac{L}{kA} = \frac{3 \times 10^{-3}}{230 \times 4 \times 10^{-4}} = 0.033 \ (K/W) \]

3. Compute the thermal resistance of TIM, $R_{TIM}$, where the thermal conductivity of the TIM is 1.5 W/(m.K) and its thickness is 0.1 mm.

   \[ R_{TIM} = \frac{L}{kA} = \frac{0.1 \times 10^{-3}}{1.5 \times 4 \times 10^{-4}} = 0.167 \ (K/W) \]

4. Compute the thermal resistance of the combined solder bumps, $R_{sb}$, which are made of 63Sn/37Pb (its thermal conductivity=50 W/(m K)). The diameter of each solder bump (spherical shape) is 1 mm. Assume that only 10 balls, each having cylindrical shape with diameter of 1 mm.

   \[ R_{sb} = \frac{L}{kA} = \frac{1 \times 10^{-3}}{50 \times [10 \times (\pi \times 0.5^2) \times 10^{-4}]} = 2.546 \ (K/W) \]

5. Compute the thermal resistance of the inter filler polymer (ifp), $R_{ifp}$ which is made of the molding compound (its thermal conductivity =0.7 W/(m K)), its thickness being the same as the height of the solder bumps, 1 mm.

   \[ R_{ifp} = \frac{L}{kA} = \frac{1 \times 10^{-3}}{0.7 \times [4 \times 10^{-4} - 10 \times (\pi \times 0.5^2) \times 10^{-6}]} = 3.643 \ (K/W) \]
(6) Compute the equivalent thermal resistance for the entire upper route, \( R_{\text{upper}} \), and the equivalent thermal resistance for the entire lower route, \( R_{\text{lower}} \),

\[
R_{\text{upper}} = R_{\text{TIM}} + R_{\text{air}} + R_{hs} = 83.532 \text{ (K/W)}
\]

\[
R_{\text{lower}} = \frac{1}{R_{sb} + \frac{1}{R_{ifp}}} = 1.5 \text{ (K/W)}
\]

(7) Compute the heat flow along the upper route, \( Q_{\text{upper}} \), and the heat flow along the lower route, \( Q_{\text{lower}} \), then temperature of the chip, \( T_{\text{chip}} \). Assume that \( Q_{\text{upper}} + Q_{\text{lower}} = Q_{\text{chip}} \).

\[
Q_{\text{upper}} = \frac{\Delta T}{R_{\text{upper}}} = \frac{T_{\text{chip}} - 25}{83.532}
\]

\[
Q_{\text{lower}} = \frac{\Delta T}{R_{\text{lower}}} = \frac{T_{\text{chip}} - 30}{1.5}
\]

\[
Q_{\text{upper}} + Q_{\text{lower}} = Q_{\text{chip}} = 120 = \frac{T_{\text{chip}} - 25}{83.532} + \frac{T_{\text{chip}} - 30}{1.5}
\]

\[
T_{\text{chip}} = 206.736 \text{C}
\]

(8) If the coolant (air in Fig. 1) is replaced by water coolant with its heat transfer coefficient, \( h = 10,000 \text{ W/(m}^2\text{ K)} \), then, calculate \( R_{\text{air}}, R_{\text{upper}}, \) and \( T_{\text{chip}} \) provide all other components’ properties being the same.

\[
R_{\text{air}} = \frac{1}{hA} = \frac{1}{10000 \times 4 \times 10^{-4}} = 0.25 \text{ (K/W)}
\]

\[
R_{\text{upper}} = R_{\text{TIM}} + R_{\text{air}} + R_{hs} = 0.45 \text{ (K/W)}
\]

\[
R_{\text{lower}} = \frac{1}{R_{sb} + \frac{1}{R_{ifp}}} = 1.5 \text{ (K/W)}
\]

\[
Q_{\text{upper}} = \frac{\Delta T}{R_{\text{upper}}} = \frac{T_{\text{chip}} - 25}{0.45}
\]

\[
Q_{\text{lower}} = \frac{\Delta T}{R_{\text{lower}}} = \frac{T_{\text{chip}} - 30}{1.5}
\]

\[
Q_{\text{upper}} + Q_{\text{lower}} = Q_{\text{chip}} = 120 = \frac{T_{\text{chip}} - 25}{0.45} + \frac{T_{\text{chip}} - 30}{1.5}
\]

\[
T_{\text{chip}} = 67.692 \text{C}
\]
Fig. 1 Vertically integrated chip, TIM, heat sink, and solder bumps, etc.

Fig. 2 Heat transfer coefficient, $h$ for air cooling.